SIP SERIES REED RELAYS

TECHNICAL SPECIFICATIONS

TYPE		SIP	
TERMINAL TYPE		PCB	
CONTACT CONFIGURATION		1 N/O	
RATED CARRYING CURRENT (RESISTIVE) AT 200 VDC / 125 VAC		0.5A (Max 200 VDC & 10 W)	
INITIAL CONTACT RESISTANCE (MAX)		0.100 Ω	
COIL NOMINAL VOLTAGES	DC	5 - 12 V	
	AC	-	
OPERATING POWER (MIN-MAX)FOR DC COIL		0.05 - 0.072 W	
DIELECTRIC STRENGTH	BETWEEN OPEN CONTACT	250 VDC	
	COIL TO CONTACT	500 VDC	
INSULATION RESISTANCE		1000 MΩ	
OPERATE TIME INCLUDING BOUNCE		1 ms	
RELEASE TIME INCLUDING BOUNCE		0.5 ms	
AMBIENT TEMPERATURE		-40˚C To + 85˚C	
LIFE EXPECTANCY		10^7 Operations at Optimum Load Conditions.	
ALL DIMENSIONS ARE IN mm (W x L x H)		10 x 22.4 x 10.5 (P)	8.6 x 24.3 x 9.5 (M)
MAX WEIGHT IN GRAMS		5 gms	
REED BREAK - DOWN VOLTAGE		200 VDC	
VIBRATION		20g, 10-1000 Hz	
SHOCK		50g, 11 ms	

SALIENT FEATURES

- Cost Effective
- Low Power Consumption
- High Capacity
- Single in Line Package

APPLICATIONS

• Modem's	• Programming	• Push Button Dialers
• Computers	• Communication	• Telemetry
• Circuit Isolation	• PF Switching	• Scanner
• Encodes & Decoder		

NOTE:-

1) All Specification / Dimensions subject to Tolerance.
2) Any Techno commercial changes is / are prerogative of manufacturer / management of the company which can be done without any notice.

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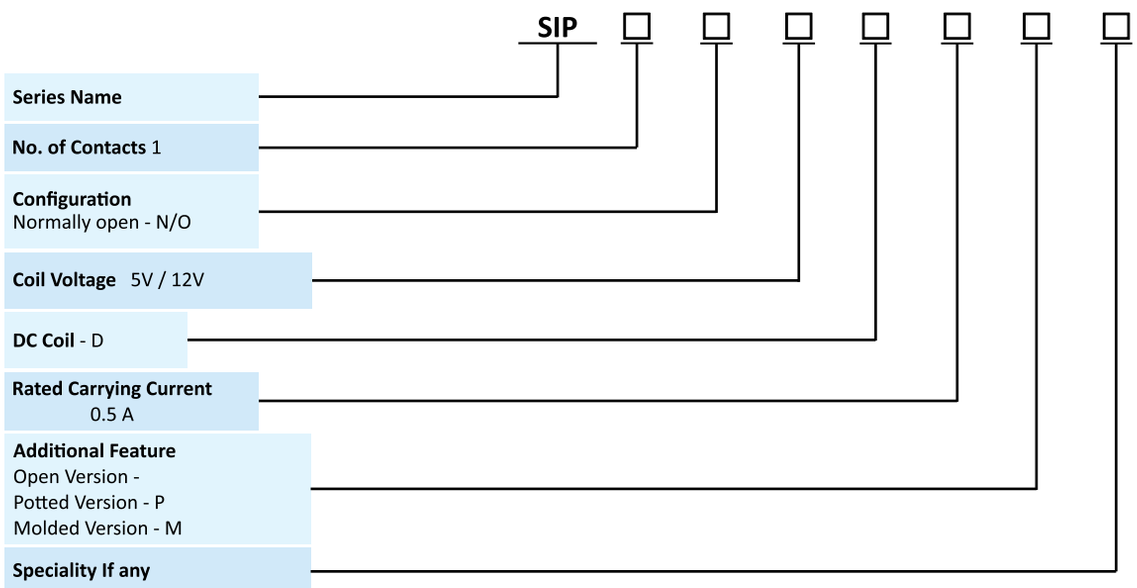
COIL – DATA (ALL VALUES AT 27˚C ± 2˚AMBIENT, COLD START)

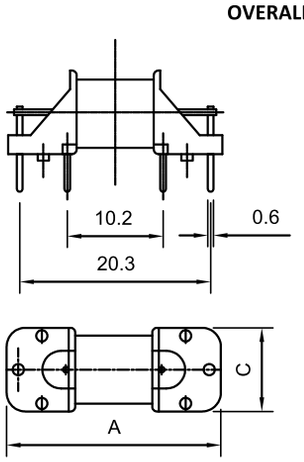
NOMINAL VOLTAGE (DC)	RESISTANCE IN OHM'S ± 10% Ω	MUST OPERATE VOLTAGE	MUST RELEASE VOLTAGE	OPERATING POWER FOR DC COIL (W)
5 V	500	4	0.5	0.05W
12 V	2k	9.6	1.2	0.072W

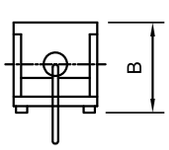
ORDERING CODE FOR RELAY



DIMENSIONS

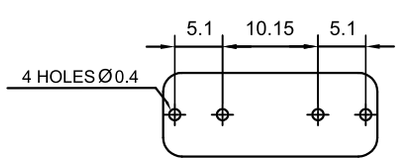
OVERALL DIMENSIONS **TYPE : SIP**





TYPE	DIMENSION IN MM.		
	A	B	C
SIP OPEN VERSION	23.2	10	9.1
SIP POTTED VERSION	22.4	10.5	10
SIP MOULDED VERSION	24.3	9.5	8.6

PCB LAYOUT



ALL DIMENSIONS ARE IN MM

NOTE :- 1) In case no tolerance shown in outline dimensions : Outline dimension 1mm, tolerance should be ±0.2mm
Outline dimension 1mm and 5mm, tolerance should be ±0.3mm Outline dimension 5mm tolerance should be±0.4mm
2) The tolerance without indicating for PCB layout is always ±0.2mm